

**1.8Ω, 650V, Super Junction N-Channel Power MOSFET**
**SRC65R1K8E**

## General Description

The Sanrise SRC65R1K8E is a high voltage power MOSFET, fabricated using advanced super junction technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and outstanding efficiency.

The SRC65R1K8E break down voltage is 650V and it has a high rugged avalanche characteristic. The SRC65R1K8E is available in TO-251, TO-252 and TO-220F packages.

## Symbol

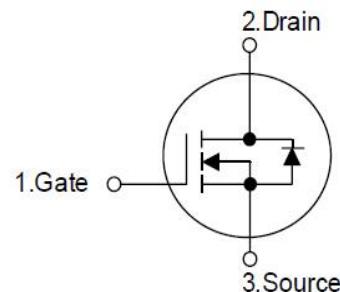


Figure 1 Symbol of SRC65R1K8E

## Package Type

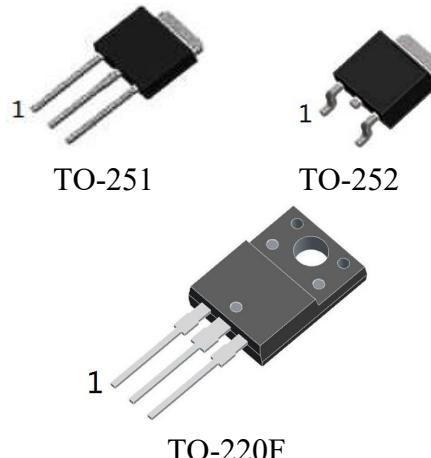
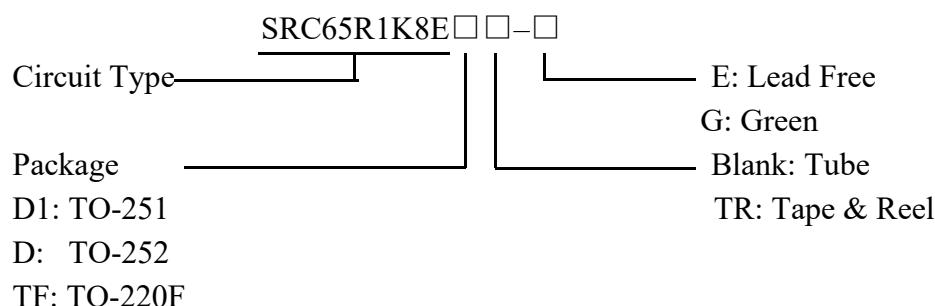


Figure 2 Package Types of SRC65R1K8E

## Ordering Information



Package	Part Number	Marking ID	Packing Type
TO-251	SRC65R1K8ED1-G	SRC65R1K8ED1G	Tube
TO-252	SRC65R1K8EDTR-G	SRC65R1K8EDG	Tape & Reel
TO-220F	SRC65R1K8ETF-G	SRC65R1K8ETFG	Tube

## Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DSS</sub>	650	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Gate-Source Voltage (AC, f>1Hz)	V <sub>GSS</sub>	±30	V
Continuous Drain Current	I <sub>D</sub>	2.7	A
T <sub>C</sub> =125°C		1.2	
Pulsed Drain Current (Note 2)	I <sub>DM</sub>	8.1	A
Avalanche Energy, Single Pulse (Note 3)	E <sub>AS</sub>	32	mJ
Avalanche Energy, Repetitive (Note 2)	E <sub>AR</sub>	0.03	mJ
Avalanche Current, Repetitive (Note 2)	I <sub>AR</sub>	0.1	A
Continuous Diode Forward Current	I <sub>S</sub>	2.7	A
Diode Pulse Current	I <sub>S,PULSE</sub>	8.1	A
Operating Junction Temperature	T <sub>J</sub>	150	°C

Note:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.  
Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. I<sub>AS</sub> = 0.1A, V<sub>DD</sub> = 60V, R<sub>G</sub> = 25Ω, Starting T<sub>J</sub> = 25°C

**1.8Ω, 650V, Super Junction N-Channel Power MOSFET**
**SRC65R1K8E**
**Electrical Characteristics**
 $T_J = 25^\circ\text{C}$ , unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Statistic Characteristics</b> <sup>NOTE1</sup>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\text{uA}$	650			V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=650\text{V}, \text{V}_{\text{GS}}=0\text{V}$			1	uA
Gate-Body Leakage Current	Forward	$\text{I}_{\text{GSSF}}$	$\text{V}_{\text{GS}}=20\text{V}, \text{V}_{\text{DS}}=0\text{V}$		100	nA
	Reverse	$\text{I}_{\text{GSSR}}$	$\text{V}_{\text{GS}}=-20\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-1.0	uA
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{TH})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\text{uA}$	2.7	3.5	4.3	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1.3\text{A}$		1.5	1.8	Ω
Gate Resistance	$\text{R}_G$	f=1MHz, Open Drain		8		Ω
<b>Dynamic Characteristics</b> <sup>NOTE2</sup>						
Input Capacitance	$\text{C}_{\text{ISS}}$	$\text{V}_{\text{DS}}=50\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1\text{MHz}$		160		pF
Output Capacitance	$\text{C}_{\text{OSS}}$			9		pF
Reverse Transfer Capacitance	$\text{C}_{\text{RSS}}$			4		pF
<b>Gate Charge Characteristics</b>						
Gate to Source Charge	$\text{Q}_{\text{gs}}$	$\text{V}_{\text{DD}}=480\text{V}, \text{I}_D=1.3\text{A}$ $\text{V}_{\text{GS}}=0 \text{ to } 10\text{V}$		1.0		nC
Gate to Drain Charge	$\text{Q}_{\text{gd}}$			2.1		nC
Gate Charge Total	$\text{Q}_g$			4.3		nC
Gate Plateau Voltage	$\text{V}_{\text{plateau}}$			5.9		V
<b>Reverse Diode Characteristics</b> <sup>NOTE2</sup>						
Drain-Source Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{SD}}=1.3\text{A}$		0.84	1.1	V



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