100mΩ, 650V, Super Junction N-Channel Power MOSFET SRC65R100B

General Description

The Sanrise SRC65R100B is a high voltage power MOSFET, fabricated using advanced super junction technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and outstanding efficiency.

The SRC65R100B break down voltage is 650V and it has a high rugged avalanche characteristics. The SRC65R100B is available in TO-220F，TO-263-2,TO-220C and TO-247 packages.

Features

- Ultra Low $R_{DS(ON)}$ = 100mΩ @ V_{GS} = 10V.
- Ultra Low Gate Charge, Qg=86nC typ.
- Intrinsic Fast-Recovery Body Diode
- Fast switching capability
- Robust design with better EAS performance
- Non-automotive Qualified
- Ultra-fast body diode

Application

- AC/DC Power Supply
- EV Charger
- PC / Sever / Telecom

Symbol

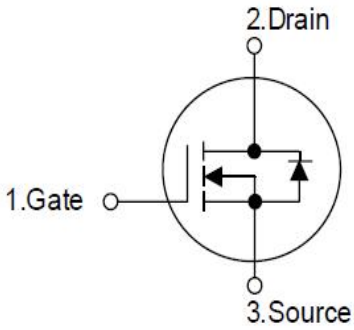

Figure 1 Symbol of SRC65R100B

Package Type

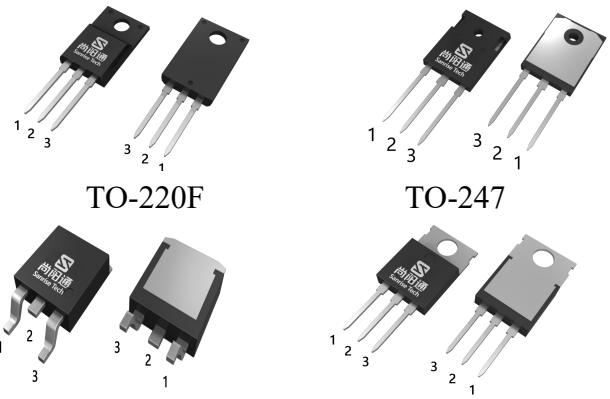

Figure 2 Package Types of SRC65R100B

Ordering Information

SRC65R100B□□–□

Circuit Type

Package
TF: TO-220F
T: TO-247
S2: TO-263-2
TC: TO-220C

E: Lead Free
G: Green
Blank: Tube
TR: Tape & Reel

Package	Part Number	Marking ID	Packing Type
TO-220F	SRC65R100BTF-G	SRC65R100BTFG	Tube
TO-247	SRC65R100BT-G	SRC65R100BTG	Tube
TO-263-2	SRC65R100BS2TR-G	SRC65R100BS2G	Tape & Reel
TO-220C	SRC65R100BTC-G	SRC65R100BTCG	Tube

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Absolute Maximum Ratings[Note 1]

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage (static)		V_{GSS}	±20	V
Gate-Source Voltage (dynamic), AC (f>1 Hz)		V_{GSS}	±30	V
Continuous Drain Current	T_C=25ºC	I_D	36.9	A
	T_C=100ºC		23.3	
	T_C=125ºC		16.5	
Pulsed Drain Current (Note 2)		I_{DM}	110.7	A
Avalanche Energy, Single Pulse (Note 3)		E_{AS}	100	mJ
Avalanche Energy, Repetitive (Note 2)		E_{AR}	0.6	mJ
Avalanche Current, Repetitive (Note 2)		I_{AR}	4.5	A
Continuous Diode Forward Current		I_S	36.9	A
Diode Pulse Current		$I_{S.PULSE}$	110.7	A
MOSFET dv/dt Ruggedness, V_{DS}<=480V		dv/dt	80	V/ns
Reverse Diode dv/dt, V_{DS}<=480V, I_{SD}<=I_D		dv/dt	50	V/ns
Power Dissipation（Tc=25℃,TO-220F）		P_{tot}	35.7	W
Power Dissipation（Tc=25℃,TO-220C,TO-263-2,TO-247）		P_{tot}	291	W
Operating Junction Temperature		T_J	150	ºC
Storage Temperature		T_{STG}	-55 to 150	ºC
Lead Temperature (Soldering, 10 sec)		T_{LEAD}	260	ºC

Note:
1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. I_{AS} = 4.5A, V_{DD} = 60V, R_G = 25Ω, Starting T_J = 25°C

Thermal characteristics

Parameter		Symbol	Min	Typ	Max	Unit
Thermal resistance, Junction-to-Case	TO-220F	R_{thJC}			3.5	ºC /W
	TO-220C				0.43	
	TO-263-2				0.43	
	TO-247				0.43	
Thermal resistance, Junction-to-Ambient	TO-220F	R_{thJA}			70	ºC /W
	TO-220C				58	
	TO-263-2				58	
	TO-247				58	

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Electrical Characteristics

T_J = 25℃, unless otherwise specified.

Parameter		Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D=250uA	650			V
Zero Gate Voltage Drain Current		I_{DSS}	V_{DS}=650V, V_{GS}=0V			10	uA
			V_{DS}=650V,V_{GS}=0V,Tj=125℃， TO-247,TO-263			1200	
Gate-Body Leakage Current	Forward	I_{GSSF}	V_{GS}=20V, V_{DS}=0V			100	nA
	Reverse	I_{GSSR}	V_{GS}=-20V, V_{DS}=0V			-100	nA
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=800uA	3.0	4.0	5.0	V
Static Drain-Source On-Resistance		$R_{DS(ON)}$	V_{GS}=10V, I_D=18A		82	100	mΩ
Gate Resistance		R_G	f=1MHz, Open Drain		0.7		Ω
Dynamic Characteristics							
Input Capacitance		C_{ISS}	V_{DS}=25V, V_{GS}=0V, f=1MHz		3333		pF
Output Capacitance		C_{OSS}			3466		
Reverse Transfer Capacitance		C_{RSS}			46.9		
Effective output capacitance, energy related NOTE5		$C_{O(er)}$	V_{GS}=0V, V_{DS}=0…400V		98.2		pF
Effective output capacitance, time related NOTE6		$C_{O(tr)}$			601		
Turn-on Delay Time		$t_{d(on)}$	V_{DD}=400V, I_D=18A R_G=1.8Ω, V_{GS}=10V		24		ns
Rise Time		t_r			38		
Turn-off Delay Time		$t_{d(off)}$			48		
Fall Time		t_f			10		
Gate Charge Characteristics							
Gate to Source Charge		Q_{gs}	V_{DD}=480V, I_D=18A V_{GS}=0 to 10V		22.9		nC
Gate to Drain Charge		Q_{gd}			42.6		
Gate Charge Total		Q_g			86		
Gate Plateau Voltage		$V_{plateau}$			6.5		V
Reverse Diode Characteristics							
Drain-Source Diode Forward Voltage		V_{SD}	V_{GS}=0V, I_{SD}=18A		0.9	1.1	V
Reverse Recovery Time		t_{rr}	V_R=100V, I_F=18A dI$_F$/dt=100A/us		152		ns
Reverse Recovery Charge		Q_{rr}			0.89		uC
Peak Reverse Recovery Current		I_{rrm}			11.6		A

Note:

5. $C_{O(er)}$ is a fixed capacitance that gives the same stored energy as C_{OSS} while V_{DS} is rising from 0 to 480V

6. $C_{O(tr)}$ is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 480 V

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