

General Description

The Sanrise SRC65R042B is a high voltage power MOSFET, fabricated using advanced super junction technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and outstanding efficiency.

The SRC65R042B break down voltage is 650V and it has a high rugged avalanche characteristics. The SRC65R042B is available in TO-247 package.

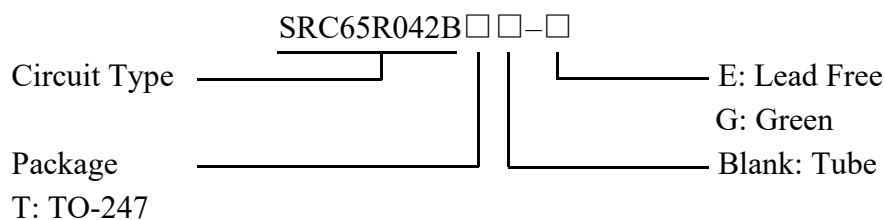
Features

- Ultra Low $R_{DS(ON)} = 42m\Omega @ V_{GS} = 10V$.
- Ultra Low Gate Charge, $Q_g = 182nC$ typ.
- Intrinsic Fast-Recovery Body Diode
- Fast switching capability
- Robust design with better EAS performance
- Non-automotive Qualified

Application

- AC/DC Power Supply
- EV Charger
- Server / Telecom
- Solar Inverter

Ordering Information



Symbol

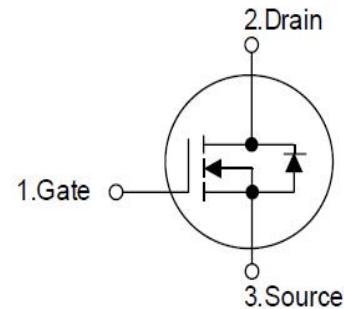
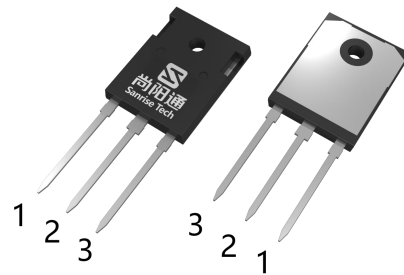


Figure 1 Symbol of SRC65R042B

Package Type



TO-247

Figure 2 Package Type of SRC65R042B

Package	Part Number	Marking ID	Packing Type
TO-247	SRC65R042BT-G	SRC65R042BTG	Tube

Absolute Maximum Ratings

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage (static)		V_{GSS}	±20	V
Gate-Source Voltage (dynamic), AC (f>1Hz)		V_{GSS}	±30	V
Power Dissipation (T _c =25°C, TO-247)		P_{tot}	520	W
Continuous Drain Current	T _c =25°C	I_D	78	A
	T _c =100°C		49.2	
	T _c =125°C		34.8	
Pulsed Drain Current (Note 2)		I_{DM}	234	A
Avalanche Energy, Single Pulse (Note 3)		E_{AS}	870	mJ
Avalanche Energy, Repetitive (Note 2)		E_{AR}	0.9	mJ
Avalanche Current, Repetitive (Note 2)		I_{AR}	6.6	A
Continuous Diode Forward Current		I_S	78	A
Diode Pulse Current		$I_{S,PULSE}$	234	A
MOSFET dv/dt Ruggedness, V _{DS} ≤480V		dv/dt	50	V/ns
Reverse Diode dv/dt, V _{DS} ≤480V, I _{SD} ≤I _D		dv/dt	50	V/ns
Operating Junction Temperature		T_J	150	°C
Storage Temperature		T_{STG}	-55 to 150	°C
Lead Temperature (Soldering, 10 sec)		T_{LEAD}	260	°C

Note:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. I_{AS} = 6.6A, V_{DD} = 120V, R_G = 25Ω, Starting T_J = 25°C

Parameter		Symbol	Min	Typ	Max	Unit
Thermal resistance, Junction-to-Case	TO-247	R _{thJC}			0.24	°C /W
Thermal resistance, Junction-to-Ambient	TO-247	R _{thJA}			54	°C /W

Electrical Characteristics

$T_J = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	650			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=650V, V_{GS}=0V$			10	μA
Gate-Body Leakage Current	Forward	$I_{GSSF}, V_{GS}=20V, V_{DS}=0V$			100	nA
	Reverse	$I_{GSSR}, V_{GS}=-20V, V_{DS}=0V$			-100	nA
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=1.6mA$	3.2	4.0	4.8	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=30A$		35	42	mΩ
Gate Resistance	R_G	f=1MHz, Open Drain		0.7		Ω
Dynamic Characteristics						
Input Capacitance	C_{ISS}	$V_{DS}=50V, V_{GS}=0V, f=1MHz$		7200		pF
Output Capacitance	C_{OSS}			281.4		
Reverse Transfer Capacitance	C_{RSS}			4.7		
Effective output capacitance, energy related ^{NOTE5}	$C_{O(er)}$	$V_{GS}=0V, V_{DS}=0\dots 400V$		155.1		pF
Effective output capacitance, time related ^{NOTE6}	$C_{O(tr)}$			905.9		
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=400V, I_D=38A, R_G=3.3\Omega, V_{GS}=10V$		17		ns
Rise Time	t_r			6.6		
Turn-off Delay Time	$t_{d(off)}$			112		
Fall Time	t_f			4.3		
Gate Charge Characteristics						
Gate to Source Charge	Q_{gs}	$V_{DD}=480V, I_D=38A, V_{GS}=0 \text{ to } 10V$		46.3		nC
Gate to Drain Charge	Q_{gd}			92.2		
Gate Charge Total	Q_g			182		
Gate Plateau Voltage	$V_{plateau}$			6.5		V
Reverse Diode Characteristics						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=38A$		0.9	1.1	V
Reverse Recovery Time	t_{rr}	$V_R=100V, I_F=38A, dI_F/dt=100A/\mu s$		168		ns
Reverse Recovery Charge	Q_{rr}			1.17		μC
Peak Reverse Recovery Current	I_{rrm}			14.0		A

Note:

- $C_{O(er)}$ is a fixed capacitance that gives the same stored energy as C_{OSS} while V_{DS} is rising from 0 to 480V
- $C_{O(tr)}$ is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 480V



Shenzhen Sanrise Technology Co., LTD

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